# Deriving a Module of a Multi Agent System via Finite State Machine Equation Solving 

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#### Abstract

In multiagent systems, there is a problem of constructing an agent that can work in different contexts satisfying different specifications. One of ways is to solve a system of corresponding automata equations. Since in general, the complexity of solving such equations is exponential w.r.t. to the number of states of the context and specification, the question arises whether a system of equations can be reduced to a single equation. In this paper, we consider two special cases when a system of equations under the parallel composition over Finite State Machines can be reduced to a single equation. For each case, it is shown how a corresponding single equation can be derived.


Keywords- multiagent systems; Finite State Machine (FSM); Parallel Composition of FSMs; FSM equations

## I. INTRODUCTION

When constructing a multiagent system there is a problem of designing an agent that can work in different contexts facing different specifications [1]. The problem about the permissible changes in the component (agent) behavior arises in many other applications, such as optimization of digital circuits, cryptography, logic synthesis, logic games, stabilizers for the synthesis of asynchronous systems, etc. The problem is known as the equation solving problem [2], submodule construction [3] or the unknown component problem [4]. If the behavior of all systems is described by sets of sequences of actions then the problem can be solved using systems of appropriate equations. In [1, 4], an approach is proposed for solving a system of equations over Finite State Machines (FSMs), i.e., over finite automata where actions are divided into inputs and outputs and each input is followed by an output. The proposed approach [1, 4] is to solve equations separately and then derive the product of the largest solutions. However, the complexity of solving a single equation over FSMs is known to have the exponential complexity with respect to the size of the context and specification and for this reason, it would be nice to check whether the problem of solving a system of FSM equations can be reduced to solving a single equation.

Usually the interaction between the components is described by using the operator of parallel or synchronous composition, and thus we have a system of parallel or synchronous equations [2]. In the case of the synchronous composition that is used in hardware, all components are
active at each time instance; the composition produces an external output and a matched pair of internal signals. The parallel composition is based on the dialogue between components, only one component is active at each time instance and an external output is produced only after finishing the internal dialogue between components. Such compositions are widely used in telecommunication systems. For multiagent systems, the parallel composition also seems to be more appropriate.

In this paper, we consider a system of finite number of equations under the parallel composition over FSMs. We show that there exist two special cases when we can reduce the system of FSM equations to a single FSM equation. Namely, it happens when all contexts or all specifications coincide.

The rest of the paper is structured as follows. In Section 2, we define basic notions and definitions for solving parallel FSM equations. Section 3 contains the problem statement and a proposed approach for reducing a system of equations to a single equation. The section also contains a short discussion about a general case. Section 4 concludes the paper.

## II. Preliminaries

## A. Finite State Machine

Finite State Machine (FSM) [5] is a quintuple $\mathrm{A}=\left(A, I, O, T_{\mathrm{A}}, a_{0}\right)$, where $A$ is a finite nonempty set of states with the initial state $a_{0}, I$ and $O$ are input and output alphabets, and $T_{\mathrm{A}} \subseteq I \times A \times A \times O$ is a transition relation. An FSM A is observable, if for each triple $(a, i, o) \in A \times I \times O$ there exists at most one state $a^{\prime} \in A$ such that $\left(a, i, o, a^{\prime}\right) \in T_{\mathrm{A}}$. An FSM A is complete, if $\forall a \in A$ and $\forall i \in I$ there exist $o \in O$ and $a^{\prime} \in A$, such that $\left(a, i, o, a^{\prime}\right) \in T_{\mathrm{A}}$. If A is not complete, then it is called partial. An FSM A is deterministic, if $\forall a \in A$ and $\forall i \in I$ there exist at most one pair of output $o$ and state $a^{\prime}$, such that $\left(a, i, o, a^{\prime}\right) \in T_{\mathrm{A}}$; an FSM A is nondeterministic, if there exist $a \in A$ and $i \in I$ such that there are two different transitions $\left(a, i, o, a^{\prime}\right),\left(a, i, o^{\prime}, a^{\prime \prime}\right) \in T_{\mathrm{A}}$. When solving FSM equations the notion of a nondeterministic FSM allows to describe a general solution to a solvable equation. An $\mathrm{FSM} \mathrm{B}=\left(B, I, O, T_{\mathrm{B}}, b_{0}\right)$ is a sub-machine of A if $B \subseteq A$ and $T_{\mathrm{B}} \subseteq T_{\mathrm{A}}$. The largest complete submachine of FSM A can be obtained by iterative deleting states where the behavior of the FSM is not defined at
least for a single input. If the initial state is deleted then FSM A has no complete machine. Otherwise, the remained complete FSM is the largest submachine of A. Each complete submachine of $A$ is a submachine of the largest complete submachine of A (if it exists). As usual, the transition relation $T_{\mathrm{A}}$ of FSM A can be extended to sequences over the alphabets $I$ and $O$. A trace of the FSM is a sequence of pairs $(i, o)$ which correspond to consecutive transitions. A trace defines an output response of the FSM to its input projection and thus, an FSM can be considered as a sequential function that maps the infinite set of input sequences to the sets of output sequences. If each state of an FSM is reachable from the initial state via some trace, then an FSM is called initially connected or simply connected. FSMs are widely used in order to describe the behavior of reactive systems which get some messages (inputs) and produce a corresponding response (output) [2]. States of the FSM correspond to the memory about previously applied inputs and produced outputs. For example, in Fig. 2 an FSM with the initial state $a$ is presented. After applying an input $i_{1}$ at the initial state $a$ the FSM produces output $u_{2}$ and moves to the next state $b$; thus, when the next input $i_{1}$ is applied the FSM produces output $o_{2}$ and remains at state $b$.

Given an FSM A, the set of all traces at state $a$ of A is called the language of A at state $a$, written $L_{A}(a)$. The language of the FSM A at the initial state is called the language of the FSM A and is denoted by $L_{\mathrm{A}}$, for short. The FSM $\left(\left\{t_{0}\right\}, I, O, T, t_{0}\right)$ where $T=\left\{t_{0}\right\} \times I \times O \times\left\{t_{0}\right\}$, written $\operatorname{MAX}(I, O)$, is called the maximum FSM over the input alphabet $I$ and the output alphabet $O$. The machine $\operatorname{MAX}(I, O)$ has the language (IO)*. An FSM B is a reduction of FSM A, written A $\leq \mathrm{B}$, if $L_{\mathrm{B}} \subseteq L_{\mathrm{A}}$. If $L_{\mathrm{B}}=L_{\mathrm{A}}$ then FSMs A and B are equivalent. For complete deterministic FSMs the reduction and the equivalence relations coincide.

The common behavior of two FSMs can be described by the intersection of these machines. The intersection (or a product) A $\cap$ B of FSMs A and B is the largest connected submachine of the FSM $\left(A \times B, I, O, T_{\mathrm{A} \cap \mathrm{B}}, a_{0} b_{0}\right)$. Formally, $T_{\mathrm{A} \cap \mathrm{B}}=\left\{\left(a b, i, o, a^{\prime} b^{\prime}\right) \mid\left(a, i, o, a^{\prime}\right) \in T_{\mathrm{A}} \wedge\left(b, i, o, b^{\prime}\right) \in T_{\mathrm{B}}\right\}$. The language of $\mathrm{A} \cap \mathrm{B}$ is the intersection $L_{\mathrm{A}} \cap L_{\mathrm{B}}$. The intersection of two observable FSMs is an observable FSM; however, the intersection of complete FSMs can be partial.

## B. Finite Automaton

When combining FSMs, all the operators are defined over finite automata. Finite automata are very close to FSMs: there is the non-empty finite set of states with the designated initial state and the designated subset of final states which correspond to finishing some job and the non-empty finite set of actions that are not divided into inputs and outputs. Given an FSM A, the corresponding finite automaton $\operatorname{Aut}(\mathrm{A})$ is derived by unfolding FSM transitions. Final states are states that have an incoming transition labeled with an output and the initial state. In Fig. 3, an automaton for the FSM in Fig. 2 is shown.

Given a sequence $\alpha \in V^{*}$ and an alphabet $W$, a $W$ restriction of $\alpha$, written $\alpha_{\downarrow W}$, is obtained by deleting from $\alpha$ all symbols that belong to the set $V \backslash W$. Given a sequence $\alpha \in V^{*}$
and an alphabet $W$, a $W$-expansion of $\alpha$, written $\alpha_{\uparrow W}$, is a set that contains each sequence over alphabet $(V \cup W)$ with the $V$-restriction $\alpha$. All the operators are extended to operators over finite automata. Let P be an automaton over alphabet $V$ with the language L. Restriction ( $\downarrow$ ): Given a non-empty subset $U$ of $V$, the automaton $\mathrm{P}_{\downarrow U}$ that accepts the language $L_{\downarrow U}$ over $U$ is obtained by replacing each transition ( $s, a, s^{\prime}$ ), $a \in V U$, in P by the transition ( $s, \varepsilon, s^{\prime}$ ). Expansion ( $\uparrow$ ): Given alphabet $U$, the automaton $\mathrm{P}_{\uparrow U}$ with the language $L_{\uparrow U}$ over $U \cup V$ is obtained by adding a transition $(s, a, s)$ for each $a \in$ $U V$ and each state $s$ of P .

## C. Parallel Composition of FSMs

Let $\mathrm{C}=\left(C, I \cup V, O \cup U, T_{\mathrm{C}}, c_{0}\right)$ and $\mathrm{X}=\left(X, U, V, T_{X}, x_{0}\right)$ be two complete communicating FSMs where alphabets $I, V$, $O, U$ are pair-wise disjoint. The alphabet $I$ represents the set of external inputs of the composition, while the alphabet $O$ represents the set of external outputs of the composition. The embedded component X corresponds to an agent to be designed.


Fig. 1. The parallel composition of FSMs $C$ and $X$
The two FSMs communicate under a single message in transit, i.e., the next external input is submitted to the system only after producing an external output to the previous input (so-called "slow" environment). Under these conditions, the collective behavior of the two communicating FSMs can be described by an FSM. When applying an external input to the FSM component C, the FSM produces an internal or an external output. If the $C$ produces an internal signal, components start their dialogue that should be finished by producing an external output by the component $C$. If the dialogue becomes infinite then the composition is said to fall into a livelock. If an external output is produced then the composition waits for the next external input.

The parallel composition of FSMs C and X [1], denoted $\mathrm{C} \diamond \mathrm{X}$, can be formally described using automata for FSM components: First, for FSMs C and X , the corresponding automata $\operatorname{Aut}(\mathrm{C})$ and $\operatorname{Aut}(\mathrm{X})$ are derived and the automaton $\operatorname{Aut}(\mathrm{X})$ is expanded to external inputs and outputs. At the next step, the intersection $\left(\operatorname{Aut}(\mathrm{C}) \cap \operatorname{Aut}(\mathrm{X})_{\uparrow I \cup o}\right)_{\downarrow I \cup O}$ is restricted to the set of external inputs and outputs and the restriction is intersected with the automaton $\operatorname{Aut}(\operatorname{MAX}(I, O))$ that models the slow environment. The obtained automaton is converted into an FSM by combining inputs with following outputs. It is known that the parallel composition of two complete FSMs
can be partial if the communicating FSMs fall into a live-lock. In this case, the $I$-restriction of $\left(\operatorname{Aut}(\mathrm{C}) \cap \operatorname{Aut}(X)_{\uparrow I \cup o}\right)_{\downarrow I \cup o} \cap \operatorname{Aut}(\operatorname{MAX}(I, \quad O))$ to may not coincide with $I^{*}$.

## D. FSM Equations

Solving the problem of designing an agent of the multiagent system, the problem of solving the system of parallel FSM equations arises. Let $\mathrm{C}=\left(C, I \cup V, O \cup U, T_{\mathrm{C}}, c_{0}\right)$ and $\mathrm{S}=\left(S, I, O, T_{\mathrm{S}}, s_{0}\right)$ be two complete FSMs. An expression "C $\diamond \mathrm{x} \cong S$ " is called an $F S M$ equation w.r.t. the unknown FSM X over the input alphabet $U$, and the output alphabet $V$. The FSM C is called the context, and the FSM $S$ is called the specification. An FSM equation can have no solution. For a solvable equation, there exists a largest solution that is the FSM with the language $\left.\operatorname{Aut}(\mathrm{C}) \diamond \overline{\operatorname{Aut}(\mathrm{S})}=(\operatorname{Aut}(\mathrm{C}) \cap \overline{\operatorname{Aut}(\mathrm{S}})_{\uparrow U \cup V}\right)_{\downarrow U \cup V}$ [2]. Undefined input sequences of the largest solution correspond to input sequences which violate the specification and for this reason, when designing an agent we are interested in complete solutions to the FSM equation. If a solvable FSM equation has a complete solution then it is known to have the largest complete solution. Each complete solution of an equation is a reduction of the largest complete solution [2].

## E. System of FSM Equations

If an agent has to work in different contexts and it is necessary to provide a certain level of service in each context then when designing an agent, the problem arises of solving systems of equations.

Let $C_{i}$ and $S_{i}$ be collections of complete FSMs where all $C_{i}$ (and all $S_{i}$ ) are defined over the same input and output alphabets. A collection of equations $\mathrm{C}_{i} \diamond \mathrm{X} \cong \mathrm{S}_{i}, i=1,2, \ldots, k$, is called a system of FSM equations. An FSM A defined over the alphabets of the unknown component X is a solution to the system if A is a solution to each equation of the system. The detailed method for solving FSM equations is described in [4] where it is also shown that a solvable FSM equation has the largest solution. A solvable system of parallel FSM equations also has the largest solution that is the intersection over largest solutions to all equations.

## III. PROBLEM STATEMENT AND A SOLUTION PROPOSED

When solving an FSM equation $\mathrm{C} \diamond \mathrm{X} \cong S$, a corresponding FSM inequality $C \diamond x \leq S$ is solved first. Given a system of FSM inequalities $\mathrm{C}_{i} \diamond X \leq S_{i}, i=1,2$, the question is whether we could reduce the system of FSM inequalities to a single FSM inequality. In other words, whether there exists a single equation such that the sets of solutions of the system and this equation coincide.

In this paper, we consider two special cases.

## A. Case 1

Let $S_{1}=S_{2}=S$, be the specification, however, this service has to be provided into two different contexts $C_{1}$ and $C_{2}$.

Theorem 1. Given a system of FSM inequalities $\mathrm{C}_{i} \diamond \mathrm{x} \leq \mathrm{S}, i=1,2$, where $\mathrm{C}_{1}$ and $\mathrm{C}_{2}$ are specified over the same alphabets, the largest solution to the system and the largest solution to the inequality $\left(C_{1} \cup C_{2}\right) \diamond \mathrm{X} \leq \mathrm{S}$ coincide.

The sketch of the proof. For each sequence $\beta$ of the context $C_{1}$ (and also $C_{2}$ ) and each sequence $\alpha$ of the largest solution M of the system $\mathrm{C}_{i} \diamond \mathrm{X} \leq \mathrm{S},(i=1,2)$ the composition $\beta \diamond \alpha$ is in the specification $S$. Since the union of the contexts $C_{1}$ and $C_{2}$ has only sequences of $C_{1}$ and $C_{2}$, for each sequence $\beta$ of the context $C_{1} \cup C_{2}$ and each sequence $\alpha$ of the largest solution $M$, the composition $\beta \diamond \alpha$ is in the specification $S$.

On the other hand, for each sequence $\alpha$ of the largest solution $M_{i}$ to a single equation $C_{i} \diamond \mathrm{X} \leq \mathrm{S}$ it holds that $\beta \diamond \alpha$ is in $S$.

The following statement can be proven by induction.
Corollary 1. Given a system of FSM inequalities $\mathrm{C}_{i} \diamond \mathrm{x} \leq \mathrm{S}, i=1,2, \ldots, k$, where $C_{1}, \ldots, \mathrm{C}_{k}$ are specified over the same alphabets, the largest solution to the system and the largest solution to the inequality $\left(C_{1} \cup \ldots \cup C_{k}\right) \diamond \mathrm{X} \leq \mathrm{S}$ coincide.

Corollary 2. Given a system of FSM equations $C_{i} \diamond \mathrm{X} \cong \mathrm{S}$, $i=1,2, \ldots, k$, where $\mathrm{C}_{1}, \ldots, \mathrm{C}_{k}$ are specified over the same alphabets and FSM $S$ is deterministic, the largest solutions to the system and to the equation $\left(\mathrm{C}_{1} \cup \ldots \cup \mathrm{C}_{k}\right) \diamond \mathrm{X} \cong \mathrm{S}$ coincide.

We now illustrate Theorem 1 by the following examples.
Example 1. Consider parallel composition in Fig. 1, contexts $C_{1}$ and $C_{2}$ in Figs. 2 and 4 and the specification $S$ with the set of transitions $\left(1, i_{1}, o_{1}, 2\right),\left(2, i_{1}, o_{2}, 2\right),\left(2, i_{2}, o_{1}\right.$, 2).


Fig. 2. The context $C_{1}$


Fig. 3. An automaton $\operatorname{Aut}\left(\mathrm{C}_{1}\right)$


Fig. 4. The context $\mathrm{C}_{2}$
The specification $S$ is defined over the set of external inputs $I=\left\{i_{1}, i_{2}\right\}$ and the set of external outputs $O=\left\{o_{1}, o_{2}\right\}$. Contexts $C_{1}$ and $C_{2}$ are defined over the set of external inputs $I=\left\{i_{1}, i_{2}\right\}$, the set of external outputs $O=\left\{o_{1}, o_{2}\right\}$, the set of internal inputs $V=\left\{v_{1}, v_{2}\right\}$ and the set of internal outputs $U=\left\{u_{1}, u_{2}\right\}$ and are shown in Figs. 2 and 4. For the context $\mathrm{C}_{2}$, there is no solution to the inequality $\mathrm{C}_{2} \diamond \mathrm{X} \leq \mathrm{S}$ and thus, there is no solution for the system of inequalities. By direct inspection, a reader can assure that for the union of $\mathrm{C}_{1}$ and $\mathrm{C}_{2}$, there also is no solution of the inequality $\left(\mathrm{C}_{1} \cup \mathrm{C}_{2}\right) \diamond \mathrm{X} \leq \mathrm{S}$.

Example 2. Consider the specification FSM $S$ with the set of transitions $\left(1, i_{1}, o_{1}, 2\right)\left(1, i_{2}, o_{2}, 1\right),\left(2, i_{1}, o_{2}, 2\right),\left(2, i_{2}, o_{1}\right.$, 2). The largest solution $L$ to the system over the set of inputs $U=\left\{u_{1}, u_{2}\right\}$ and the set of outputs $V=\left\{v_{1}, v_{2}\right\}$ derived as the intersection of largest solutions to the inequalities $C_{1} \diamond \mathrm{X} \leq \mathrm{S}$ and $C_{2} \diamond \mathrm{X} \leq \mathrm{S}$ is shown in Fig. 5. Consider now the inequality $\left(C_{1} \cup C_{2}\right) \diamond X \leq S$. By direct inspection one can assure that the largest complete solution $L^{\prime}$ to the inequality $\left(\mathrm{C}_{1} \cup \mathrm{C}_{2}\right) \diamond \mathrm{X} \leq \mathrm{S}$ is equivalent to the FSM in Fig. 5.


Fig. 5. The largest complete solution $L$ to the system

## B. Case 2

Let $C_{1}=C_{2}=C$ be the context; however, different services have to be provided in this context.

Theorem 2. Given a system of FSM inequalities $\mathrm{C} \diamond \mathrm{X} \leq \mathrm{S}_{i}, i=1,2$, where $\mathrm{S}_{1}$ and $\mathrm{S}_{2}$ are specified over the same alphabets, the largest solution to the system and the largest solution to the inequality $\mathrm{C} \nabla \mathrm{X} \leq\left(\mathrm{S}_{1} \cap \mathrm{~S}_{2}\right)$ coincide.

The sketch of the proof. For each sequence $\beta$ of the context $C$ and each sequence $\alpha$ of the largest solution $M$ of the system $C \diamond \mathrm{X} \leq S_{i},(i=1,2)$, the composition $\beta \diamond \alpha$ is in the specification $S_{1}$ (and also is in $S_{2}$ ). Since the intersection of specifications $S_{1}$ and $S_{2}$ has only sequences which are in both
$S_{1}$ and $S_{2}$, for each sequence $\beta$ of the context $C$ and each sequence $\alpha$ of the largest solution $M$, the composition $\beta \diamond \alpha$ is in $S_{1} \cap S_{2}$.

On the other hand, for each sequence $\alpha$ of the largest solution $M_{i}$ to a single equation it holds that $\beta \diamond \alpha$ is in $S_{1} \cap S_{2}$.

The following statement can be proven by induction.
Corollary 1. Given a system of FSM inequalities $\mathrm{C} \diamond \mathrm{x} \leq \mathrm{S}_{i}, i=1,2, \ldots, k$, where $\mathrm{S}_{1}, \ldots, \mathrm{~S}_{k}$ are specified over the same alphabets, the largest solution to the system and the largest solution to the inequality $\mathrm{C} \diamond \mathrm{x} \leq\left(S_{1} \cap \ldots \cap S_{k}\right)$ coincide.

Corollary 2. Given a system of FSM equations $\mathrm{C} \diamond \mathrm{X} \cong S_{i}$, $i=1,2, \ldots, k$, where $S_{1}, \ldots, S_{k}$ are deterministic, the largest solution to the system and the largest solution to the equation $\mathrm{C} \diamond \mathrm{X} \cong\left(S_{1} \cap \ldots \cap S_{k}\right)$ coincide.

## IV. CONCLUSION

In this paper, we have studied the problem of reducing the system of parallel FSM equations to a single equation that arises in many applications, such as the optimization of digital circuits, cryptography, logic synthesis, logic games, stabilizers for the synthesis of asynchronous systems, etc. In particular, we have considered two special cases when such reduction can be performed. For each case, it is shown how to derive a corresponding single equation. A general case, when contexts and specifications of different equations do not coincide, needs more research and this is our future work. The same approach can be applied for the general case of parallel composition when both, the context and the unknown have external input and outputs. We also mention that the system of equations over the synchronous composition can be treated in the same way; the synchronous composition operator corresponds to the case when all components are active at each time instance and is used for describing the behavior of hardware modular systems.

## Acknowledgment

This work is partly supported by Project 739 (goszadanie minobrnayki RF).

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